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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79162

Shinya SHIMASAKI

Group Art Unit: 2818

Appln. No.: 10/750,970 ✓

Examiner: Trong Q. PHAN

Confirmation No.: 1868

Patent No.: 6,826,085

Filed: January 5, 2004

Issue Date: November 30, 2004

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE CAPABLE OF ACCURATELY
AND QUICKLY ADJUSTING STEP-UP VOLTAGE

REQUEST FOR CERTIFICATE OF CORRECTION

ATTN: Certificate of Correction Branch

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of
Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not
enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our
Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

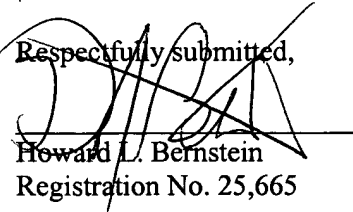
SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

Date: April 13, 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO: 6,826,085
DATED: November 30, 2004
INVENTOR(S): Shinya SHIMASAKI

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2, line 20, after "CL." insert --NG is a value of voltage that is not near enough to the exemplary voltage V_{ppe} to be acceptable. OK is a value of voltage that is near enough to the exemplary voltage V_{ppe} to be acceptable--.

MAILING ADDRESS OF SENDER:
SUGHRUE MION, PLLC

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

PATENT NO. 6,826,085

No. of additional copies